广告位

安防监控摄像头购买/电气智能化工程安装可联系-超低价

无线AP覆盖/音响安装/智慧门禁/车牌识别

（电子与智能化二级资质）

廉价版智能沙盘/展厅移动机器人可联系

山西本地特种车辆租赁/混凝土/买车买房/抵押贷款需要可联系

办理各类企业/工程师人工智能工信部证书可联系（包过一条龙）

高光谱相机/100kg大型吊装无人机购买可联系

山西本地无人机高空清洗作业可联系/数字人视频生成

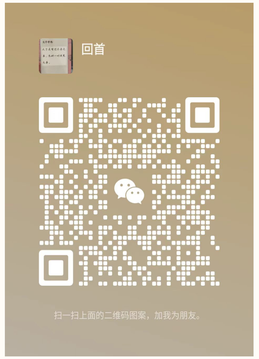
ERP沙盘模拟赛可dd/高校实验室装修/化工电子类实验设备

项目科技查新/双软评估/科小认证等包过一条龙可联系

企业代理记账/知识产权代缴/企业科技项目申报可联系

中国创翼创业创新大赛等各类企业创赛可代做（PPT/申报书/知识产权一条龙）

有以上需要均可dd我，介绍人来给提成！



办证

认证企业	方向	周期/方式	证书实物图
英伟达NVIDIA认证	人工智能计算证书	一周内全包下证	
腾讯云	交付工程师	一周内全包下证	
	云计算技能认证 区块链从业认证		
	腾讯高级工程师-数据库产品认证		
华为云	鲲鹏/华为云 技能认证	一周内全包下证	
	开发者工作级认证		
	大数据技能认证 hicp证书		
	openGauss数据库开发者证书		

	A	B
1	项目类别	认证专业
2	商务管理类	人力资源管理师
3		风险管理师
4		项目管理师
5		物联网管理师
6		新媒体运营师
7		大数据分析师
8		互联网营销师
9		数字经济管理师
10		电子商务师
11		电子政务师
12		社会工作师
13	医疗防疫公共卫生类	消毒员
14		公共场所卫生管理员
15	美学艺术类	化妆品测评师
16		新媒体主播
17		美业培训师
18		美容院管理师
19		摄影摄像与后期制作
20	经贸管理类	酒店经营管理师
21		理财规划制师
22		连锁经营管理师
23		双碳管理师
24		财务税务管理师
25		工商企业管理师
26		公共关系管理师
27		微商经营管理师
28		市场营销师
29	计算机与软件	劳动关系协调师
30		计算机信息技术
31		企业培训师
32		管理会计师
33		中式京调师
34		中式面点师

序号	专业名称	级别
1	广告艺术设计师	高级
2	网络工程师	高级
3	电子商务师	高级
4	室内设计师	高级
5	平面设计师	高级
6	ERP财务应用师	高级
7	JAVA软件工程师	高级
8	计算机辅助设计师	高级
9	软件技术开发工程师	高级
10	软件开发工程师	高级
11	物联网技术应用工程师	高级
12	数据库管理工程师	高级
13	软件设计师	高级
14	网络安全架构师	高级
15	渗透测试工程师	高级
16	提示工程师	高级
17	大模型开发工程师	高级
18	数字技术应用工程师	高级
19	大数据治理工程师	高级
20	数据安全工程师	高级
21	程序设计工程师	高级
22	AIGC应用工程师	高级
23	大模型应用工程师	高级
24	大数据采集工程师	高级
25	商务数据分析师	高级
26	移动安全工程师	高级
27	安全运维工程师	高级
28	软件工程师	高级
29	数据库工程师	高级
30	音响工程	高级
31	舞台灯光	高级
32	通信工程师	高级
33	软件测试工程师	高级
34	新能源应用工程师	高级
35	新材料检测工程师	高级
36	新材料研发工程师	高级
37	新材料技术与应用工程师	高级
38	固体废物资源综合利用技术工程师	高级
39	现场管理工程师	高级
40	网络信息与安全工程师	高级
41	机器学习工程师	高级
42	python技术应用工程师	高级

企业认证证书 | 工信部职业 | 工信部人才交流中心测评证书 | 工

类别	编号	测评科目
计算机与软件	CPOO1	Office办公软件
	CPOO2	信息化办公
	CPOO3	计算机硬件维护
	CPOO4	系统管理
	CPOO8	设备管理
	CPOO9	网站管理
	CPO10	物流管理
	CPO11	网络营销
	CPO12	局域网组建
	CPO13	网络工程师
	CPO15	信息安全与管理
	CPO17	电子商务师
	CPO19	跨境电子商务师
	CPO20	数据管理
	CPO21	数据库应用技术
	CPO22	C语言编程高阶
	CPO24	.NET开发工程师
	CPO25	Android软件开发工程师
	CPO26	Java软件开发工程师
	CPO27	J2EE软件开发工程师
	CPO31	Web前端开发工程师
	CPO42	软件测试工程师
	CPO206	财务信息化管理
	CPO45	平面设计师
	CPO47	网页设计师
	CPO48	二维动画设计师
	CPO49	三维设计师

企业认证证书 | 工信部职业 | 工信部人才交流中心测评证书 | 工信部信创证书 | 中国未来研究会 | 人力

以下说明来源为数据手册内容

SH1106的1.3寸OLED性能参数

2. *Absolute Maximum Ratings*

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	V_{DD}	-0.3	4	V	1, 2
Supply Voltage for Display	V_{CC}	0	14	V	1, 2
Supply Voltage for DC/DC	V_{BAT}	*-0.3*	*5*	*V*	*1, 2*
Operating Temperature	T_{OP}	-40	85	°C	
Storage Temperature	T_{STG}	-40	85	°C	3
Life Time (120 cd/m²)		10,000	-	hour	4
Life Time (80 cd/m²)		30,000	-	hour	4
Life Time (60 cd/m²)		50,000	-	hour	4

Note 1: All the above voltages are on the basis of "V_{SS} = 0V".
Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 3. "Optics & Electrical Characteristics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.
Note 3: The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80°C.
Note 4: V_{CC} = 12.0V, T_A = 25°C, 50% Checkerboard.
Software configuration follows Section 4.4 Initialization.
End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

3. *Optics & Electrical Characteristics*

3.1 Optics Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Brightness (V_{CC} Supplied Externally)	L_{br}	Note 5	120	-	-	cd/m²
Brightness (V_{CC} Generated by Internal DC/DC)	L_{br}	*Note 6*	*100*	*150*	-	*cd/m²*
C.I.E. (White)	(x) (y)	C.I.E. 1931	0.28 0.31	0.32 0.35	0.36 0.39	
Dark Room Contrast	CR		-	2000:1	-	
Viewing Angle			-	Free	-	degree

* Optical measurement taken at V_{DD} = 2.8V, V_{CC} = 12V & *7.25V*.
Software configuration follows Section 4.4 Initialization.

3.2 DC Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	V_{DD}		1.65	2.8	3.3	V
Supply Voltage for Display (Supplied Externally)	V_{CC}	Note 5 (Internal DC/DC Disable)	-	12.0	-	V
Supply Voltage for DC/DC	V_{BAT}	*Internal DC/DC Enable*	*3.5*	-	*4.2*	*V*
Supply Voltage for Display (Generated by Internal DC/DC)	V_{CC}	*Note 6 (Internal DC/DC Enable)*	*6.4*	-	*9*	*V*
High Level Input	V_{IH}	I_{OUT} = 100μA, 3.3MHz	0.8×V_{DD}	-	V_{DD}	V
Low Level Input	V_{IL}	I_{OUT} = 100μA, 3.3MHz	0	-	0.2×V_{DD}	V
High Level Output	V_{OH}	I_{OUT} = 100μA, 3.3MHz	0.9×V_{DD}	-	V_{DD}	V
Low Level Output	V_{OL}	I_{OUT} = 100μA, 3.3MHz	0	-	0.1×V_{DD}	V
Operating Current for V_{DD}	I_{DD}		-	180	300	μA
Operating Current for V_{CC} (V_{CC} Supplied Externally)	I_{CC}	Note 7	-	23	32	mA
Operating Current for V_{CC} (V_{CC} Generated by Internal DC/DC)	I_{AV}	*Note 8*	-	*45*	*50*	*mA*
Sleep Mode Current for V_{DD}	$I_{DD, SLEEP}$		-	1	5	μA
Sleep Mode Current for V_{CC}	$I_{CC, SLEEP}$		-	2	10	μA

Note 5 & *6*: Brightness (L_{br}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.
Note 7: V_{DD} = 2.8V, V_{CC} = 12V, IREF=910K 100% Display Area Turn on.
Note 8: V_{DD} = 2.8V, V_{CC} = 8V, IREF=560K 100% Display Area Turn on.
* Software configuration follows Section 4.4 Initialization.

以下说明来源为数据手册内容

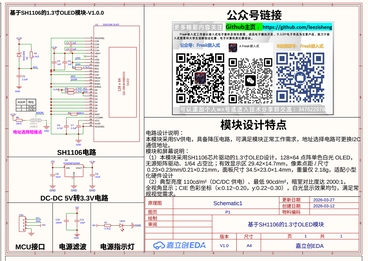
SH1106的1.3寸OLED引脚说明

1.5 Pin Definition

Pin Number	Symbol	I/O	Function
Power Supply			
9	VDD	P	**Power Supply for Logic** This is a voltage supply pin. It must be connected to external source.
8	VSS	P	**Ground of Logic Circuit** This is a ground pin. It acts as a reference for the logic pins. It must be connected to external ground.
28	VCC	P	**Power Supply for OEL Panel** This is the most positive voltage supply pin of the chip. A stabilization capacitor should be connected between this pin and V_{SS}, when the converter is used. It must be connected to external source when the converter is not used.
29	VLSS	P	**Ground of Analog Circuit** This is an analog ground pin. It should be connected to V_{SS} externally.
Driver			
26	IREF	I	**Current Reference for Brightness Adjustment** This pin is segment current reference pin. A resistor should be connected between this pin and V_{SS}. Set the current at 12.5uA maximum.
27	VCOMH	O	**Voltage Output High Level for COM Signal** This pin is the input pin for the voltage output high level for COM signals. A capacitor should be connected between this pin and V_{SS}.
DC/DC Converter			
6	VBAT	P	**Power Supply for DC/DC Converter Circuit** This is the power supply pin for the internal buffer of the DC/DC voltage converter. It must be connected to external source when the converter is used. It should be connected to V_{CC} when the converter is not used.
4 / 5 2 / 3	C1P / C1N C2P / C2N	I	**Positive Terminal of the Flying Inverting Capacitor** **Negative Terminal of the Flying Boost Capacitor** The charge-pump capacitors are required between the terminals. They must be floated when the converter is not used.
Interface			
10 11 12	BS0 BS1 BS2	I	**Communicating Protocol Select** These pins are MCU interface selection input. See the following table: BS0 BS1 BS2 I²C 0 1 0 3-wire SPI 1 1 0 4-wire SPI 0 0 0 8-bit 6800X Parallel 0 0 1 8-bit 8080X Parallel 0 1 1
14	$\overline{CS}$#/OSCEN	I	**Power Reset for Controller and Driver** This pin is reset signal input. When the pin is low, initialization of the chip is executed. Keep this pin pull high during normal operation.
13	CS#	I	**Chip Select** This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.
15	D/C#	I	**Data/Command Control** This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 is treated as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. When the pin is pulled high and serial interface mode is selected, the data at SDIN will be interpreted as data. When it is pulled low, the data at SDIN will be transferred to the command register. In I²C mode, this pin acts as SA0 for slave address selection. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams.
17	E/RD#	I	**Read/ Write Enable or Read** This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low. When serial or I²C mode is selected, this pin must be connected to V_{SS}.

1.5 Pin Definition (Continued)

Pin Number	Symbol	I/O	Function
Interface (Continued)			
16	R/W#	I	**Read/Write Select or Write** This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as R/W# (W#) input. Pull this pin to "High" for read mode and pull it to "Low" for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low. When serial or I²C mode is selected, this pin must be connected to V_{SS}.
18~25	D0~D7	I/O	**Host Data Input/Output Bus** These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCL. When I²C mode is selected, D2 & D1 should be tired together and serve as SDA$_{out}$ & SDA$_{in}$ in application and D0 is the serial clock input SCL. Unused pins must be connected to V_{SS} except for D2 in serial mode.
Reserve			
7	N.C.	-	**Reserved Pin** The N.C. pin between function pins are reserved for compatible and flexible design.
1, 30	N.C. (GND)	-	**Ground Pin (Supporting Pin)** The supporting pins can reduce the influences from stresses on the function pins. These pins must be connected to external ground as the ESD protection circuit.



版本记录

V1.0.0版本-2026/03/12-零高幸：初版完成设计

以下说明来源为数据手册内容

SH1106的1.3寸OLED 简介

1. *Basic Specifications*

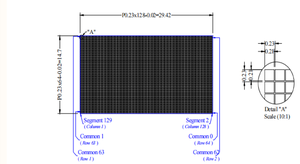
1.1 Display Specifications

1) Display Mode: Passive Matrix
2) Display Color: Monochrome (White)
3) Drive Duty: 1/64 Duty

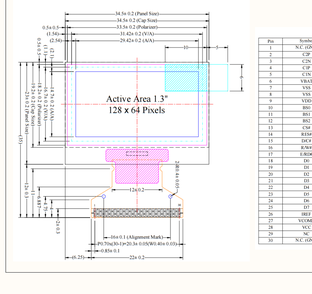
1.2 Mechanical Specifications

1) Outline Drawing: According to the annexed outline drawing
2) Number of Pixels: 128 x 64
3) Panel Size: 34.5 x 23.0 x 1.4 (mm)
4) Active Area: 29.42 x 14.7 (mm)
5) Pixel Pitch: 0.23 x 0.23(mm)
6) Pixel Size: 0.21 x 0.21(mm)
7) Weight: 2.18 (g)

1.3 Active Area / Memory Mapping & Pixel Construction



SH1106的1.3寸OLED尺寸和引脚图



SH1106的1.3寸OLED规格书

1. *Basic Specifications*

1.1 Display Specifications

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